Call for Papers

The 2nd International Symposium on Software Fault Prevention, Verification, and Validation (SFPVV 2025)

8 - 9 November 2025, Shanghai, China

HP: https://sfpvv2025.world/

Important Dates

Full/Regular Paper Track
   Submissions: *25 August 2025*
   Notification: *29 September 2025*
   Camera-ready: *20 October 2025*

Presentation Track
   Submissions: *15 September 2025*
   Notification: *8 October 2025*

Symposium Organizers

Steering Committee
   Tadashi Dohi
   Hiroshima University
   Shaoying Liu (Chair)
   East China Normal University
   Geguang Pu
   East China Normal University
   Tatsuhiro Tsuchiya
   University of Osaka

General Chair
   Shaoying Liu
   East China Normal University

Program Committee
   Yuting Chen (Chair)
   Shanghai Jiaotong University
   Hiroyuki Okamura (Chair)
   Hiroshima University
   * Members are listed on the homepage

Local Arrangement Chair
   Weikai Miao
   East China Normal University

Web and Registration Chairs
   Min Zhang
   East China Normal University
   Yuxiang Shang
   Hiroshima University

Background and Objectives

Software fault prevention, verification, and validation are essential and important approaches to ensuring software productivity, reliability, and qualities. Fault prevention focuses on the issues of how to prevent the introduction and occurrence of faults in software systems. Verification is a means to rigorously check whether software systems satisfy their specification or properties. Validation refers to the technologies and activities for confirming whether the behavior and performance of software systems satisfy the user's requirements. Various techniques and supporting tools have been developed to fulfill the tasks of software fault prevention, verification, and validation, but many difficulties and open problems remain unaddressed.

This symposium aims to invite researchers and practitioners working on software quality assurance to exchange ideas and to discuss how formal approaches, testing-based approaches, AI approaches, and their combinations can be studied, established, and supported to fulfill the goals of fault prevention, verification, and validation. All people who are interested in the topics mentioned above are welcome to submit papers and to participate in the symposium.

Scope and Topics

Authors are invited to submit technical papers describing original and unpublished work on, but not limited to, the following topics:

Specification and Prototyping	AI and Code Generation
Specification and Testing	AI and Testing
Refinement and Evolution	AI and Verification
Formal Verification and Review	Human-Machine Pair Programming
Testing-Based Formal Verification	Human-Machine Pair Review
Bug Prediction and Reliability	Supporting Tools
Modeling	Case studies
AI and Specification	Application experience

Submission and Publication

Two types of submissions are possible: **presentation papers** and **regular papers**. Both types of papers should be written in English. All papers should be submitted using the EasyChair link:?. Accepted presentation papers will not be included in the proceedings of the symposium; only the accepted regular papers will be included in the proceedings, which will be published in the Springer LNCS series. The details of the symposium can be found in the symposium homepage.

- A **regular paper** must not have been published or be concurrently considered for publication elsewhere. Each regular paper should not exceed **18 pages** (including references) in the Springer LNCS format.
- A **presentation paper** is not required to report mature research results but expected to include new ideas or progress in ongoing research projects. Each presentation paper must provide a clear title and an extended abstract in **2 pages** in the Springer LNCS format.

Cooperation

East China Normal University, China

Hiroshima University, Japan